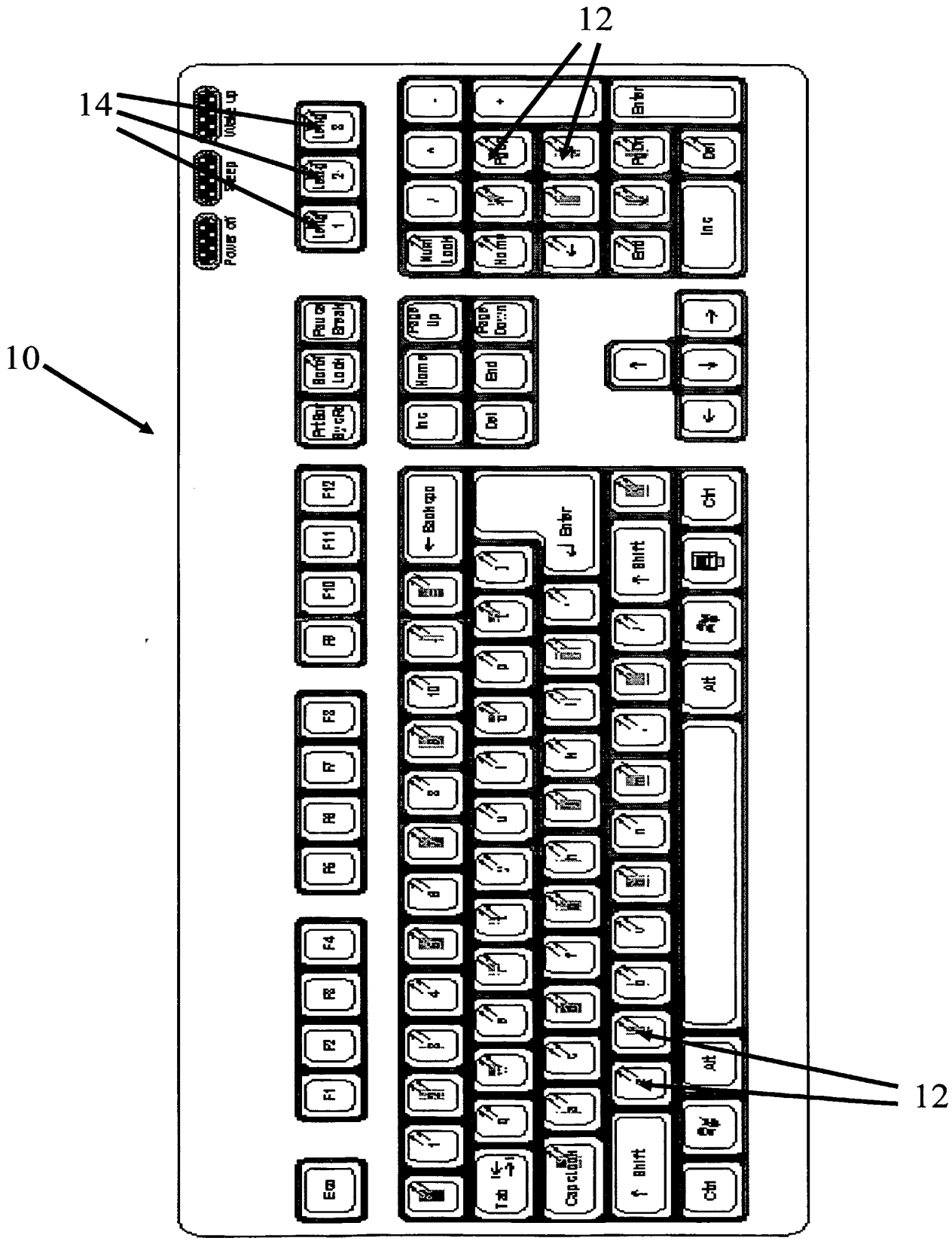
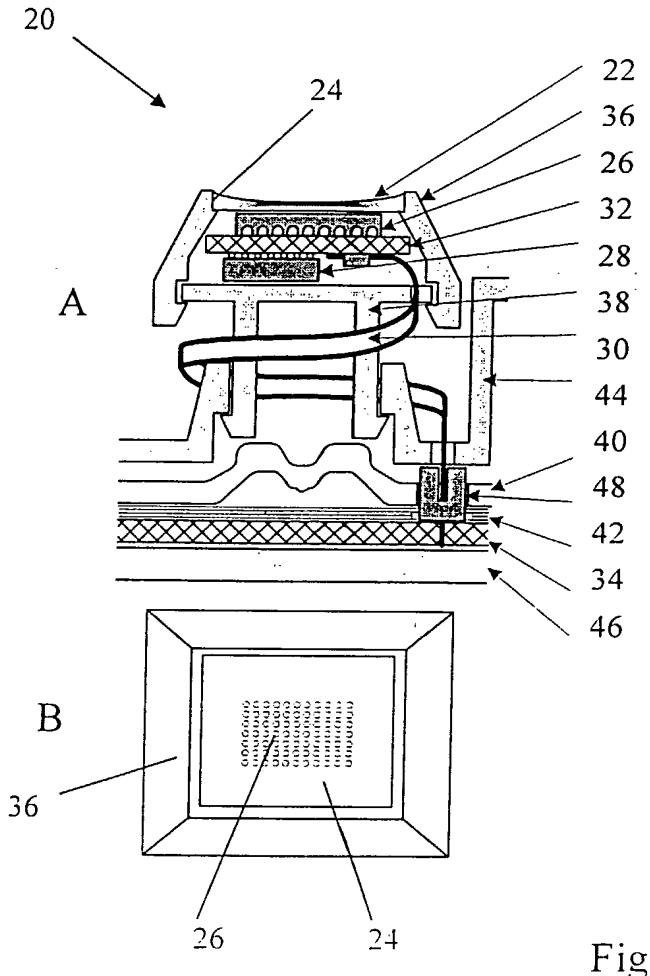


Figure 1



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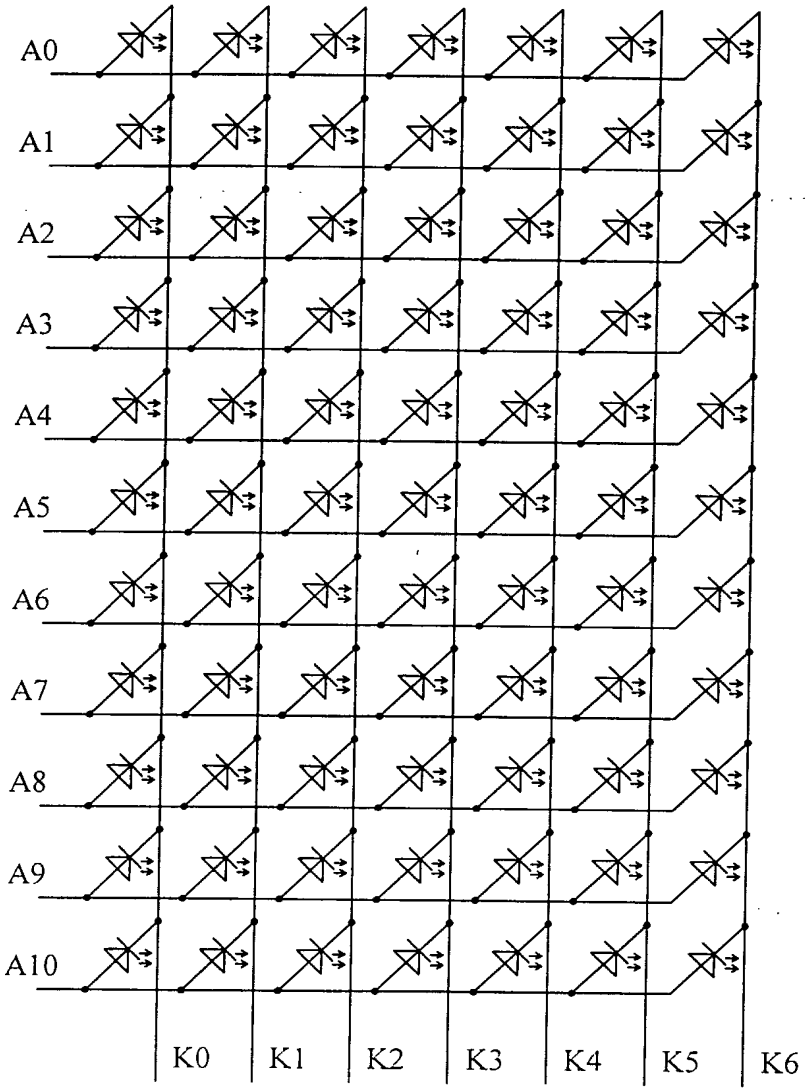

Figure 3

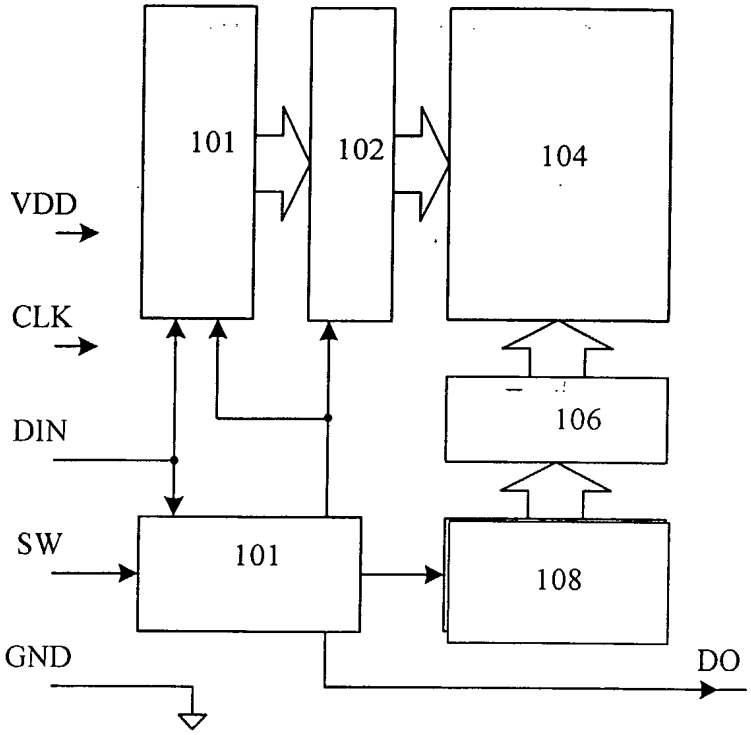

Figure 4

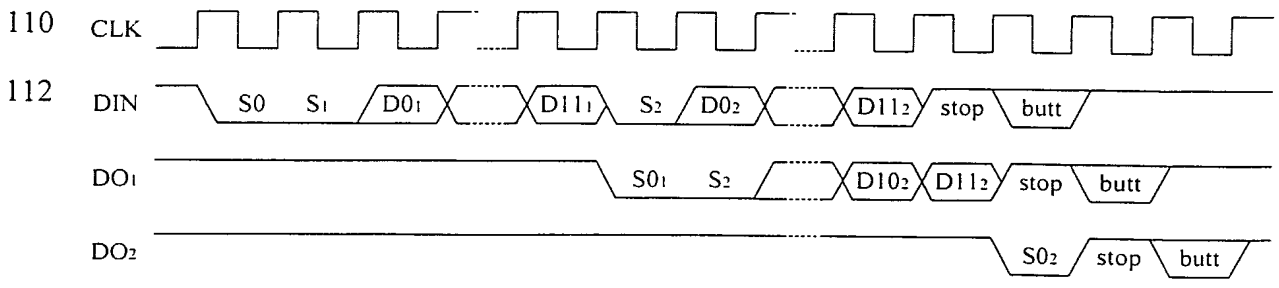

Figure 5

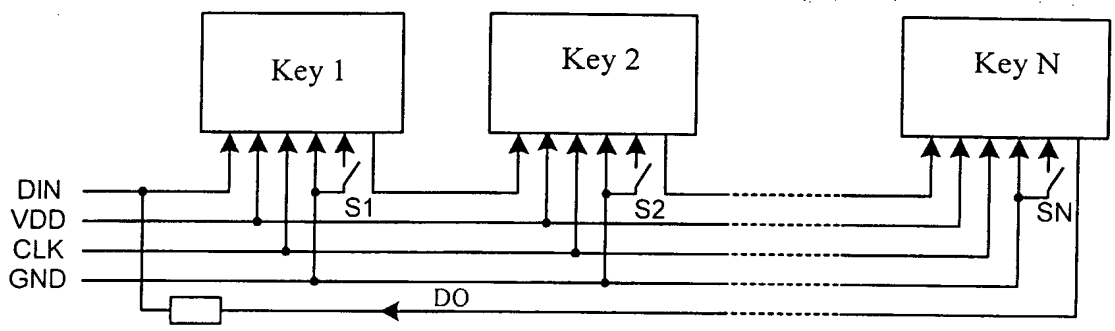

Figure 6

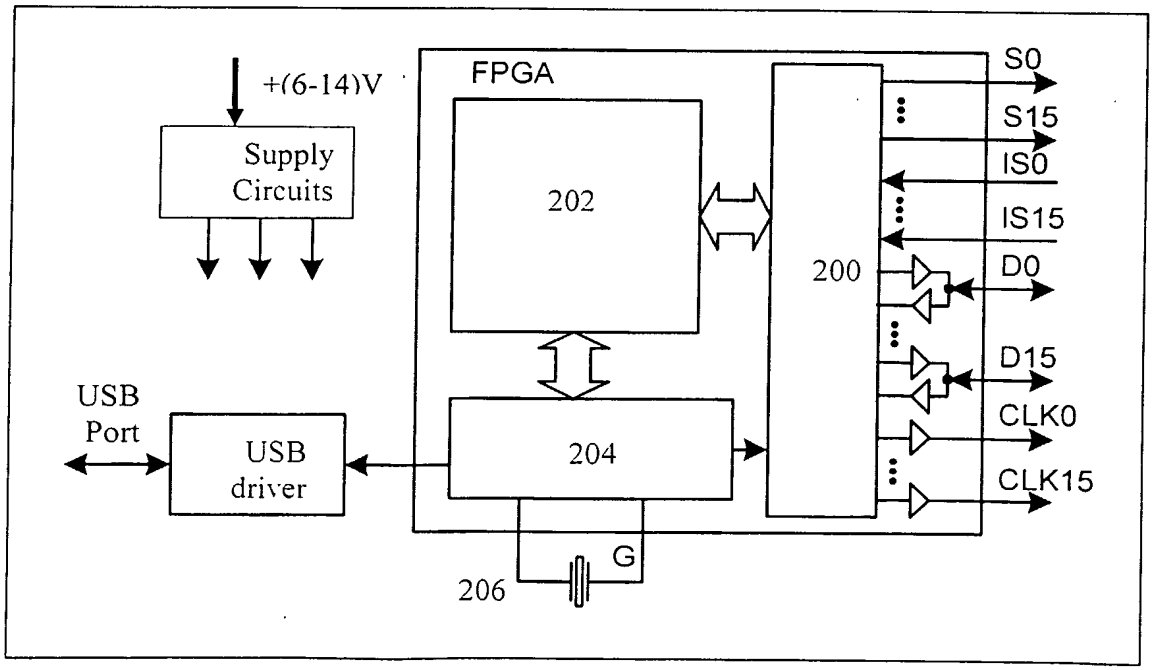

Figure 7

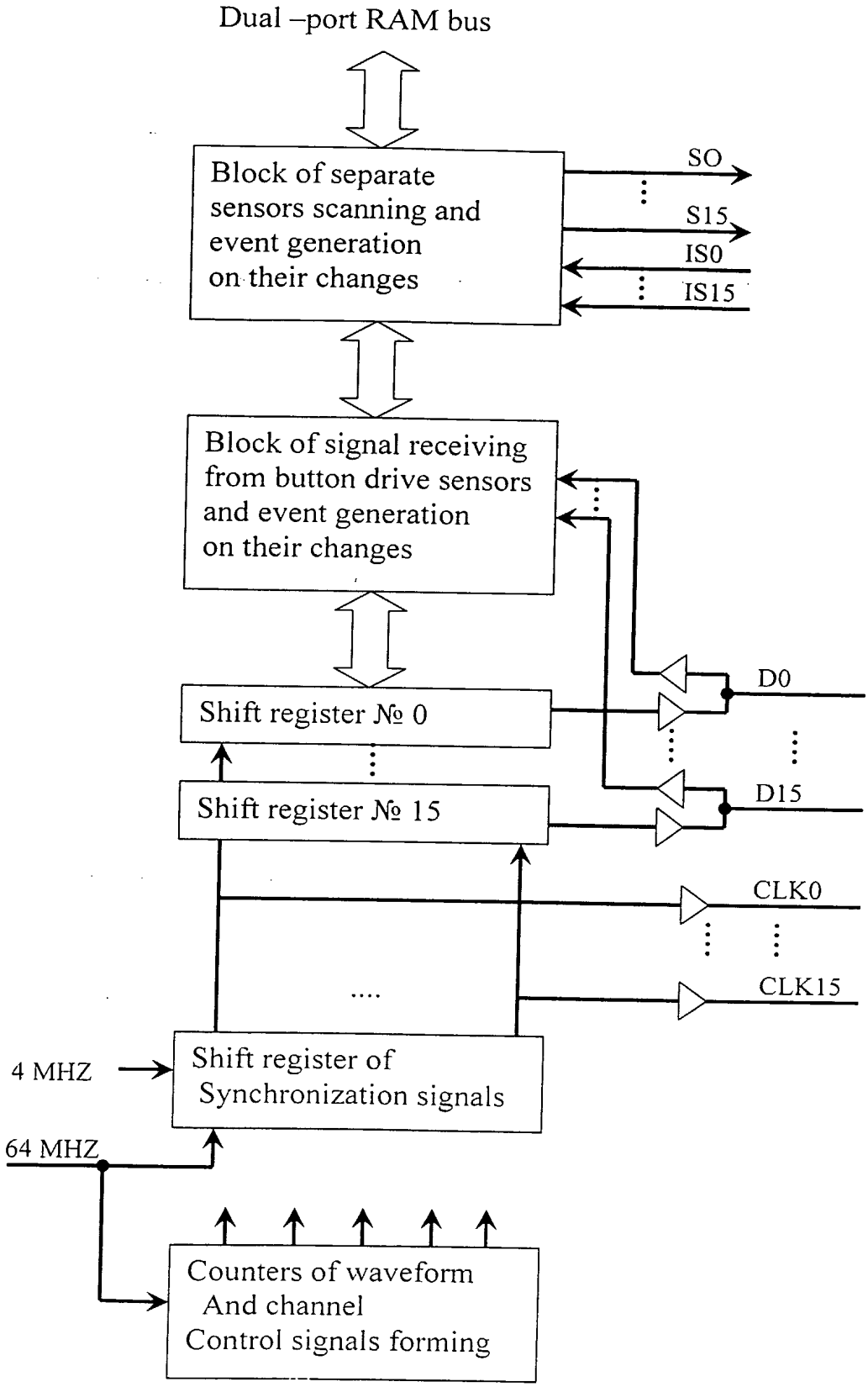
Dual –port RAM bus
Block of separate sensors scanning and event generation on their changes
SO
S15
IS0
IS15
Block of signal receiving from button drive sensors and event generation on their changes
D0
D15
Shift register № 0
Shift register № 15
CLK0
CLK15
....
4 MHZ
Shift register of Synchronization signals
64 MHZ
Counters of waveform And channel Control signals forming

Figure 8